

ADAPTIVE COMPUTING ENGINE

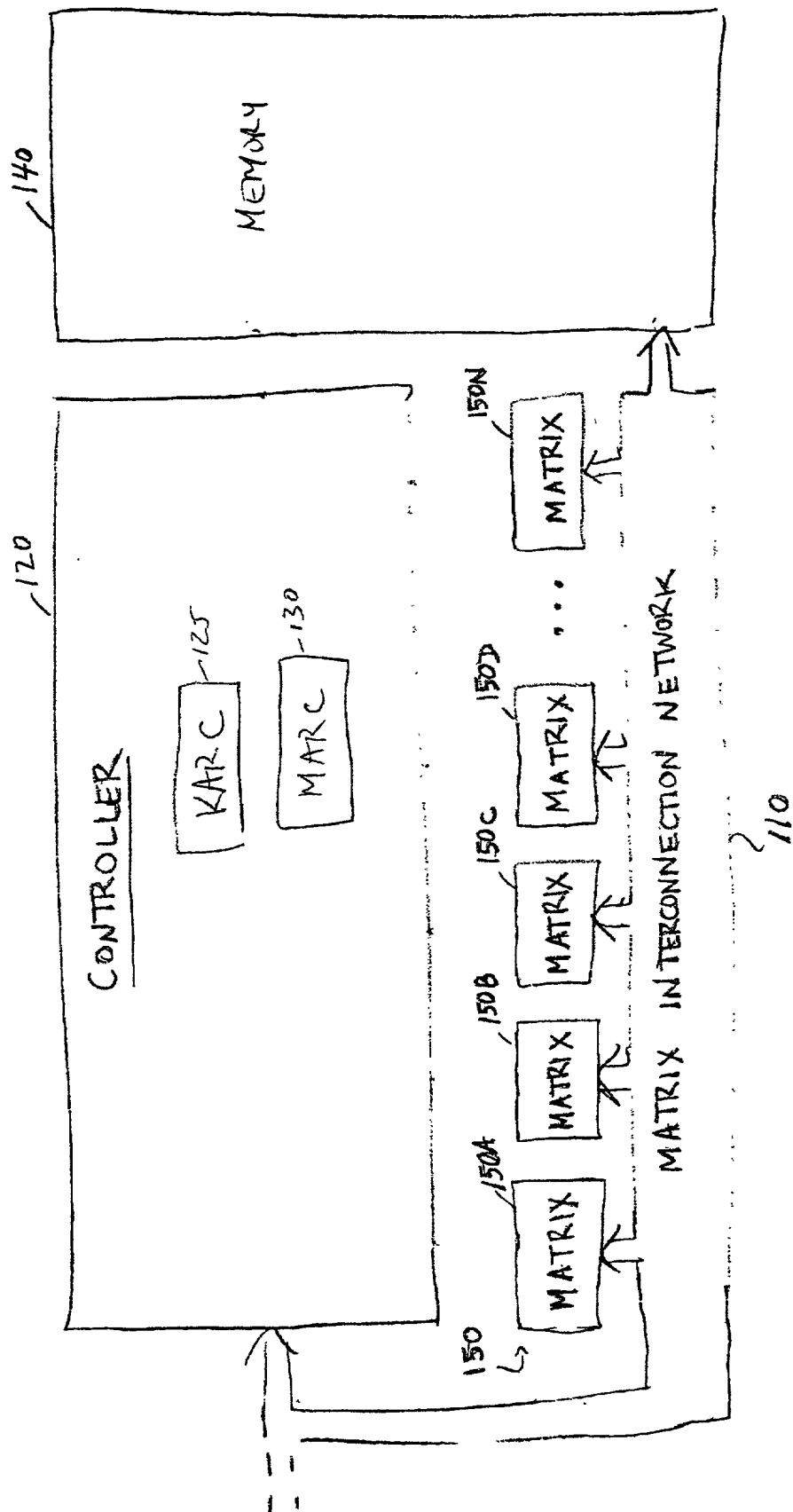


FIG. 1

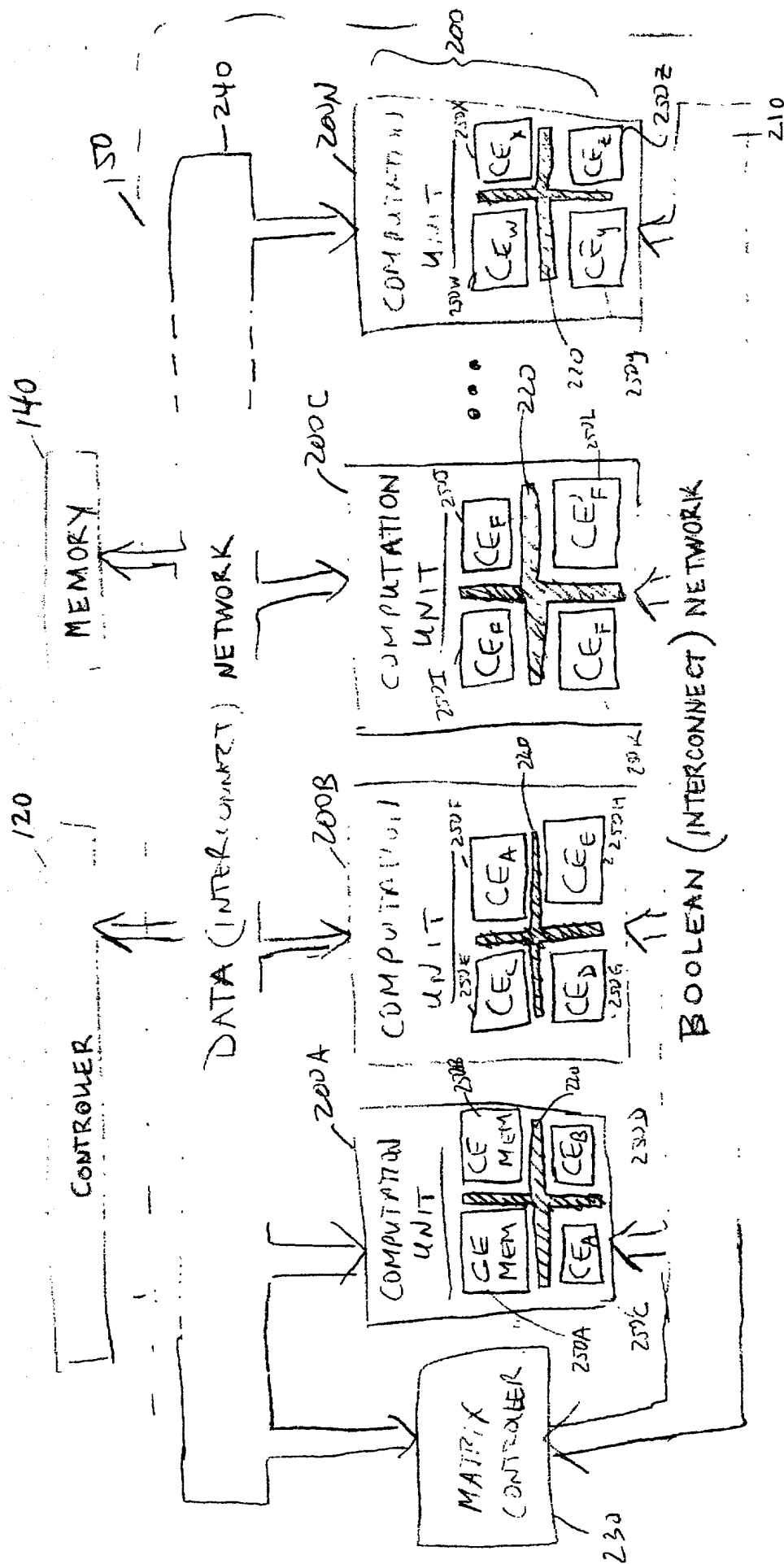
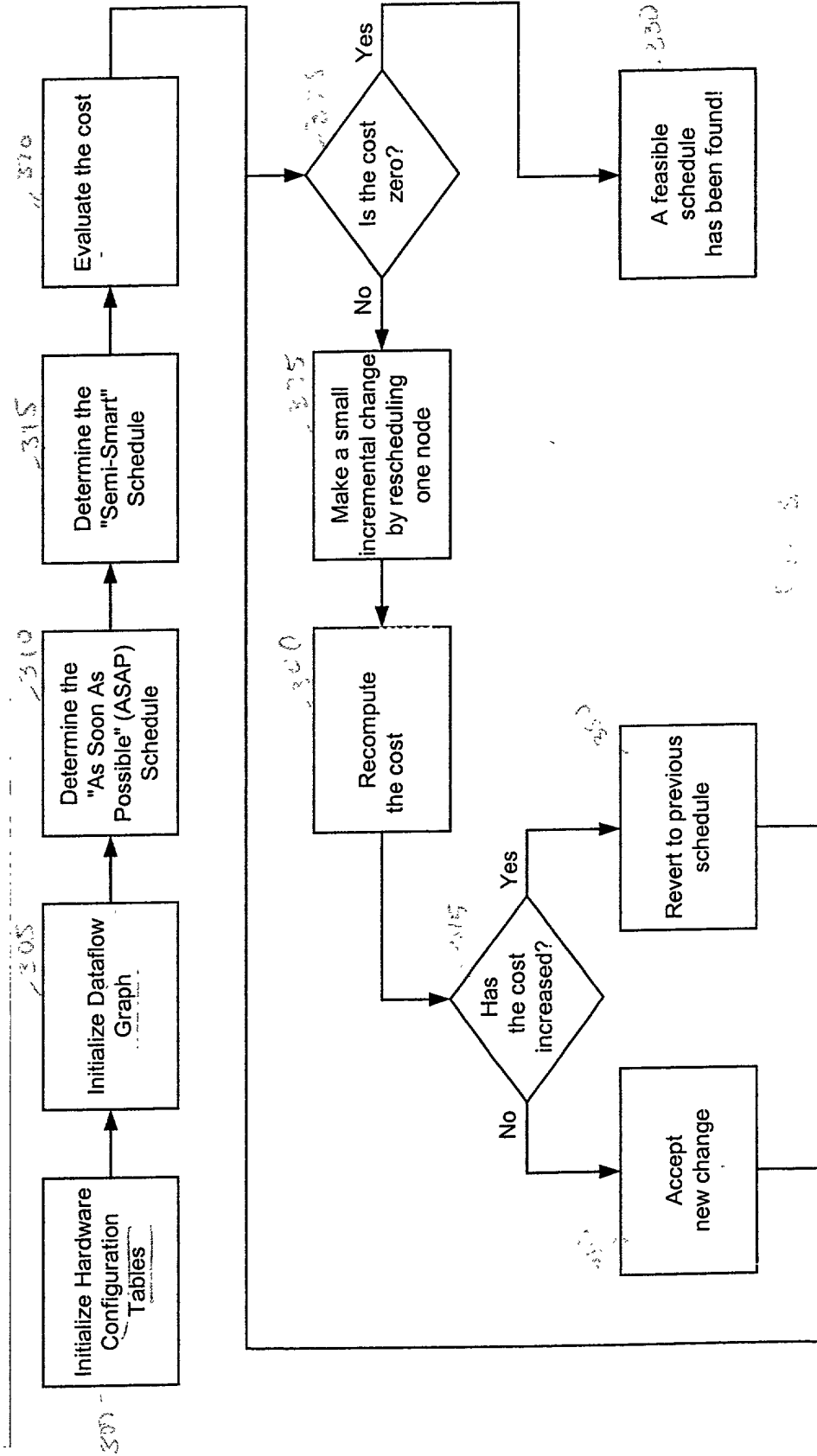


FIG. 2 is a schematic diagram of a system architecture.



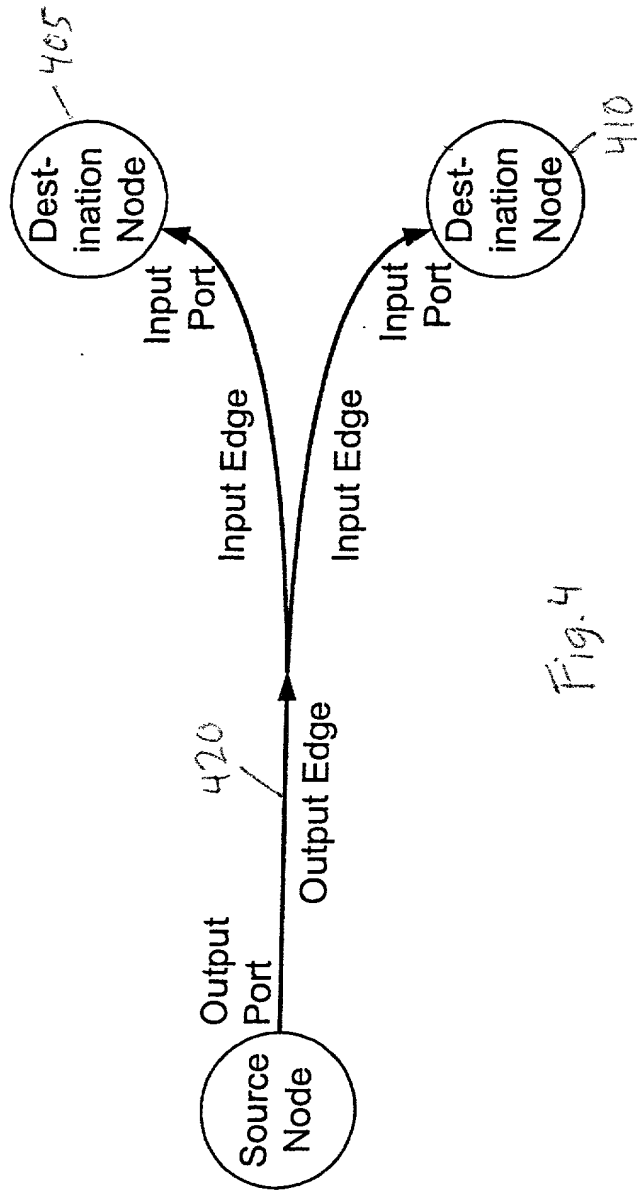


Fig. 4